Intermittent- and Wipe/Wash Control for Wiper Systems

Description

With the U264xB, TEMIC Semiconductors developed a family of intermittent- and wipe/wash control circuits for windshield or backlite wiper systems with identical basic functions. The circuit design provides the possibility to

Features

- Relay activation can be controlled by a limit switch of the wiper motor or by a fixed activation period for systems without limit switch
- Debounced input stages
- Enable/disable of pre-wash delay by program pin
- Polarity of WIWA: V_{Batt}
- Polarity of INT: V_{Batt}
- Relay output is protected with a clamping diode

Ordering Information

generate "x" versions using different metallization masks. Thus, it is easy to verify a broad range of time sequences which can be set independently of each other.

- Relay activation: 0.48 s
- Interval pause: 5.8 s
- After wiping: 5.2 s
- Pre-wash delay:) 0.52 s
- Wipe/wash mode with priority
- Protected in accordance to ISO/TR 7637–1
- EMC with intergrated filters

Extended Type Number	Package	Remarks
U2640B	DIP8	
U2640B-FP	SO 8	

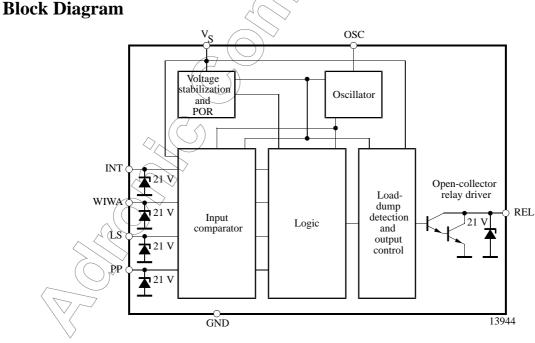


Figure 1.

Pin	Symbol	Function
1	INT	Intermittent input
2	WIWA	Wipe/wash (WIWA) input
3	LS	Limit switch (wiper motor) input
4	PP	Program pin
5	GND	Ground
6	REL	Relay output
7	Vs	Supply voltage
8	OSC	RC oscillator input

Functional Description

All times specified below refer to an oscillator frequency of 200 Hz. Figures 9 to 16 show the dependencies of the times upon battery voltage and temperature. The temperature dependence of the oscillator frequency is essentially determined by the temperature coefficient of the oscillator capacitor. The temperature dependence of the oscillator frequency can be reduced to minimum with a slightly negative temperature coefficient (N100). The capacitor used in figures 10 and 11 has a slightly positive temperatur coefficient.

All times are permanently set and can be changed only jointly within certain limits by adjusting the oscillator frequency. See table 1.

Intermittent Function

The relay is energized for the time t_{ON} after the switch INT is switched on with respect to V_{Batt} and after expiration of time t_D (debounce).

The debounce time ranges between 60 ms and 80 ms. A time period of 5 ms to 40 ms for internal sequence control must be added (asynchronism between operating instant and internal clock) e.g., the response time may range from 65 ms up to 120 ms.

If the limit switch of the windscreen wiper motor is connected to Pin **LS**, the relay is energized as long as the switch is at high potential, regardless of the relay on-time, t_{ON} , i.e., the motor current in interval mode flows via the relay contact only. In park position, the motor winding at both ends is connected to ground via the limit switch and the motor is decelerated immediately. The limit switch input is debounced with $t_{DL} = 17$ ms.

The relay on-time, t_{ON} , always elapses – even if the interval switch was opened beforehand.

LS 3 PP 4 Figure 2_Pinning

Interval Pause

INT

WIWA

The interval pause $t_{INT} = 5.8$ s follows t_{ON} . Opening of switch **INT** causes a debounce time, t_D , and reclosing results in the relay on-time, t_{ON} , after t_D .

Semiconductor

8

OSC

RÈ

GND

13365

Wipe/Wash Function without Pre-Wash Delay (PP connected to GND)

The water pump is switched on when the switch **WIWA** is pressed and, after the debounce time, t_D , the relay is energized. After-wiping time $t_{AW} = 5.2$ s starts as soon as switch **WIWA** is opened and the debounce time, t_D , has expired. If the limit switch is connected, the relay remains energized until the wiper arm returns to park position, i.e., the motor current flows via the relay contact only.

Wipe/Wash Function with Pre-Wash Delay (PP connected to V_S)

In wipe/wash mode, the relay is energized after a delay time. The water pump can spray water onto the wind-screen during the delay time, $t_{\rm DEL}$.

The on-delay time of the U2640B is:

$$t_{DEL} = t_D + 0.44 \ s = 0.52 \ s$$

If switch **WIWA** is closed longer than t_D but shorter than t_{DEL} , the after-wiping time, t_{AW} , starts after expiration of t_{DEL} . The wipe/wash function with or without on-delay t_{DEL} can be selected by programming PP.

The after-wiping time, t_{AW} , is re-triggerable in both cases.

Intermittent and Wipe/Wash Mode

The wipe/wash function has priority over the interval function. If switch **WIWA** is closed during the interval

function, wipe/wash mode is activated immediately after the debounce time, t_D , even if an on-delay is programmed ($t_{DEL} = 0$ s). Expiry of t_{AW} is directly followed by the next relay on-time, t_{ON} , of intermittent mode.

Oscillator

All timing sequences are derived from an RC-oscillator whose charging time, t_1 , is determined by an external resistor, R_{OSC} , and whose discharging time, t_2 , is determinated by an integrated 2-k Ω resistor. Since tolerance and temperature response of the integrated resistor are far higher than those of the external resistor, t_1/t_2 must be selected to be greater than 20 for stability reasons. The minimum value of R_{OSC} should not be less than 68 k Ω .

Calculating cycle duration and frequency:

 $\label{eq:t1} \begin{array}{l} t=t_1+t_2=C_{OSC}\,\times\,(~0.74\,\times\,R_{OSC}+2260~\Omega)\\ \text{and} \quad f_{OSC}=1/t \end{array}$

Calculating the capacitor for a given resistor: $C_{OSC} = t / (0.74 \times R_{OSC} + 2260 \Omega)$

Calculating the oscillator resistance for a given capacitor: $R_{OSC} = 1.34 \times (t / C_{OSC} - 2260 \ \Omega)$

Recommended frequency: $f_{OSC} = 200 \text{ Hz}$ (for $R_{OSC} = 200 \text{ k}\Omega$, $C_{OSC} = 33 \text{ nF}$)

All times can be varied jointly within specific limits by varying the oscillator frequency (see table 1). The oscillator is operable up to 50 Hz.

Table 1.	Change in	times b	у у	varying	the	oscillator frquency

Power Supply

For reasons related to protection against interference and destruction, the Pin V_S must be provided with an RC network for limiting the current in the event of overvoltage and for buffering in the event of voltage drops.

Proposed ratings: $R_V = 510 \Omega$, $C_V = 47 \mu F$. An integrated 14-V Zener diode is connected between V_S and GND.

Interference Voltages and Load-Dump

In the case of transients, the integrated Zener diode limits the voltage of the relay output to approximately 28 V. In the case of load-dump, a current (dependent upon R_V and C_V) flows through the integrated 14-V Zener diode, and the relay output is switched on at $V_{Batt} > 30$ V in order to avoid destruction of the output. The output transistor is rated such that it can withstand the current generated during the load-dump through the relay coil. In practice, the windscreen wiper motor is switched on via the relay and thus the amplitude of the load-dump pulse is limited. The supply voltage of the circuit is limited to 14 V by the integrated Zener, diode, and the inputs are protected by external protective resistors and integrated Zener diodes.

RF suppression is implemented with a low-pass filter at the inputs, consisting of a protective resistor and the integrated capacitor.

Power-on Reset (POR)

When the supply voltage is applied, a power-on reset pulse is generated which sets the circuit's logic to a defined initial state. The POR threshold is approximately $V_S = 4.3$ V.

fosc (Hz)	t _D [ms]	t _{DL} [ms]	t _{ON} [ms]	t _{INT} [s]	t _{AW} [s]	t _{DEL} [s]
100	140	(35)	960	11.84	10.24	920
120	116	29	800	9.68	8.53	766
140	100	7 25	686	8.45	7.31	657
160	87 (🤇	<u> </u>	600	7.40	6.40	575
180	77	19	533	6.57	5.68	511
200	70	17	480	5.92	5.12	460
220	64	16	436	5.38	4.65	418
240	(58)	14	400	4.93	4.26	383
260	54	13	370	4.55	3.94	353
280	50	12	343	4.23	3.66	328
300	46	11	320	3.95	3.41	306
400	35	9	240	2.96	2.56	230

Absolute Maximum Ratings

With recommended external circuitry

Parameter	Test Conditions	Symbol	Value	Unit
Supply voltage (static)	5 min	V _{Batt}	24	V
Supply current pulse	2 ms	Is	1.5	A
Supply current pulse	300 ms	IS	150	mA
Relay output current (static)		I _{REL}	300 🔨	mA
Relay output current pulse	300 ms	I _{REL}	1.5	А
Ambient temperature range		T _{amb}	-40 to ±95	°C
Storage temperature range		T _{stg}	-55 to +125	°C
Power dissipation	DIP8	P _{tot}	((0.45))	W
Power dissipation	SO8	P _{tot}	0.34	W

Thermal Resistance

Р	Parameters	Symbol	Value	Unit
Junction ambient	DIP8	R _{thJA}	120	K/W
Junction ambient	SO8	R _{thJA}	160	K/W
		(7/2)		

Electrical Characteristics

Reference point Ground GND, $T_{amb} = 25^{\circ}C$, $V_{Batt} = 13.5$ V, unless otherwise specified (see figures 11 and 12)

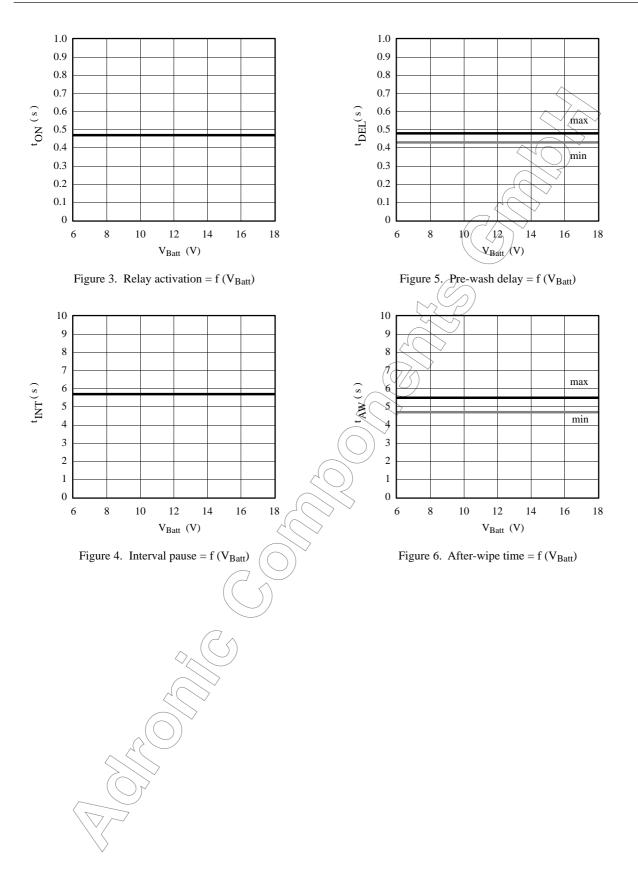
Test Conditions / Pin	Symbol	Min	Тур	Max	Unit			
Voltage supply Pin 7								
$\langle \langle \langle \rangle \rangle$	V Batt	6.0		16.0	V			
	<u> </u>	0.5	2.0	3.0	mA			
	$\sim v_s$	3.0		5.1	V			
	VZ	13.5	14.0	16.2	V			
	CS		15		pF			
	R _V		510		Ω			
$(\langle \rangle)$	C_V		47		μF			
Pin 8								
\square	R _{DIS}	1.3	2.0	3.2	kΩ			
\sim (\subset \checkmark)	V _{OSC}	$0.16 \times V_S$	$0.20 \times V_S$	$0.24 \times V_S$	V			
	V _{OSC}	$0.55 \times V_S$	$0.60 \times V_S$	$0.65 \times V_S$	V			
$V_{OSC} = 0 V$	-I _{OSC}			2	μΑ			
	f _{OSC}	1	200	50 k	Hz			
Pin 3								
$I_{LS} = 10 \text{ mA}$	V _{LS}	19.5	21.0	25.5	V			
	C _{LS}		25		pF			
	V _{LS}	$0.375 \times V_S$	$0.5 \times V_S$	$0.675 \times V_S$	V			
$V_{LS} = V_S$				-	μΑ			
	R _{LS}	13	20	27	kΩ			
	R _S	10			kΩ			
	Pin 7 Pin 7 Pin 8 Pin 8 V _{OSC} = 0 V Pin 3	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $			

TEMIC Semiconductors

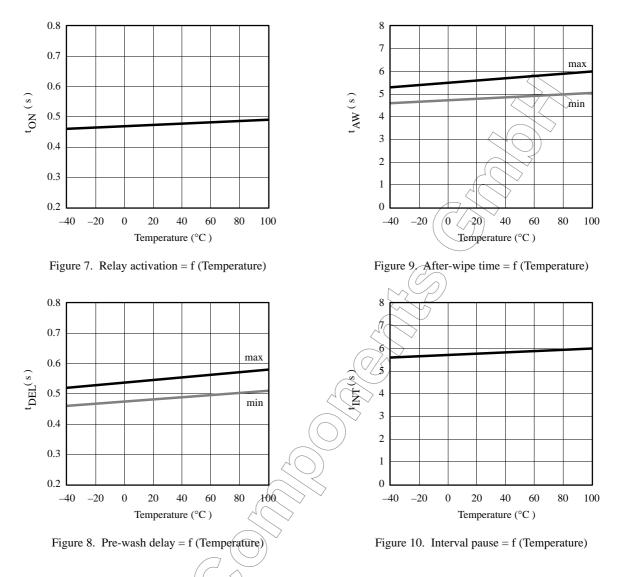
Parameters	Test Conditions / Pin	Symbol	Min	Тур	Max	Unit	
Inputs INT, WIWA and PP Pins 1, 2 and 4							
Internal protection-diode	$I_E = 10 \text{ mA}$	V _E	19.5	21.0	25.5	V	
voltage					~		
Internal capacitor		C _E		25		pF	
Switching threshold		V _E	$0.375 \times V_S$	$0.5 \times V_S$	0.675×Vs	V	
voltage							
Input current	$V_E = 0 V$	$-I_E$		\sim		μΑ	
Internal pull-down resistor		R _E	13	20	27	kΩ	
External protection resistor		R _S	10		\sim	kΩ	
Relay output	Pin 6				\sim		
Saturation voltage	I = 100 mA	V _{REL}			1.1	V	
Saturation voltage	I = 200 mA	V _{REL}			1.5	V	
Z-diode clamp voltage	I = 10 mA	V _{REL}	19.5	21/.0	25.5	V	
Leakage current	V = 14 V	I _{REL}			12	μΑ	
Relay coil resistance		R _{REL}	607			Ω	
Load-dump protection		V _{Batt}	(28)	33	42	V	
threshold			$\sum \sum D$				
Internal pulse times		(
Debouncing period inputs	INT/WIWA 12 - 16 clocks	$t_{\rm D}$	60	70	80	ms	
Debouncing period inputs	LS $3-4$ clocks	tDL	> 15	17.5	20	ms	
Relay activation time	96 clocks	ton		480		ms	
Intermittent pause		LINT		5.92		S	
After wiping period	1024 ± 68 clocks	CtwiwA	4.78		5.46	S	
Pre-wash delay, reaction	88 – 96 clocks	t DEL	440		480	ms	
time for switch-on delay =	((D)					
$t_{DEL} + t_D$							

Note: All internally generated time sequences are derived from the oscillator frquency. The tolerances refer to a frequency adjusted to $f_{OSC} = 200 \text{ Hz}$.

U2640B







Note: The temperature characteristic is caused by the temperature coefficient T_C of the external capacitor

TELEFUNKEN Semiconductors Rev. A2, 02-Dec-97

Application Examples

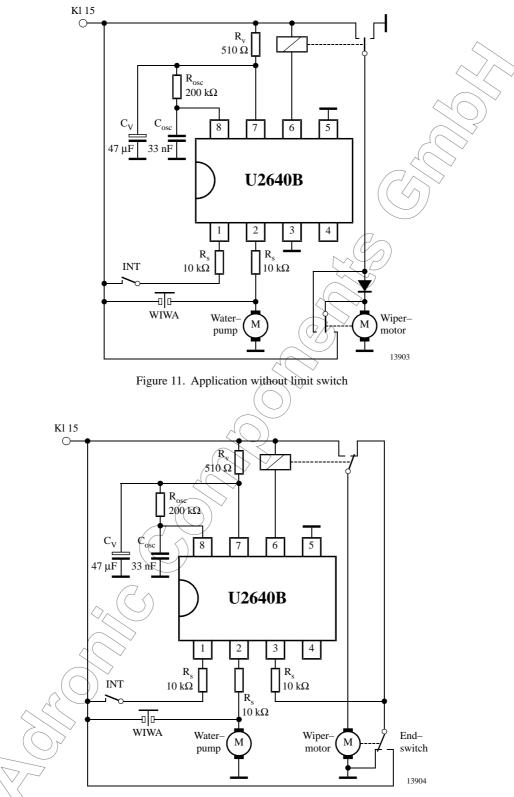


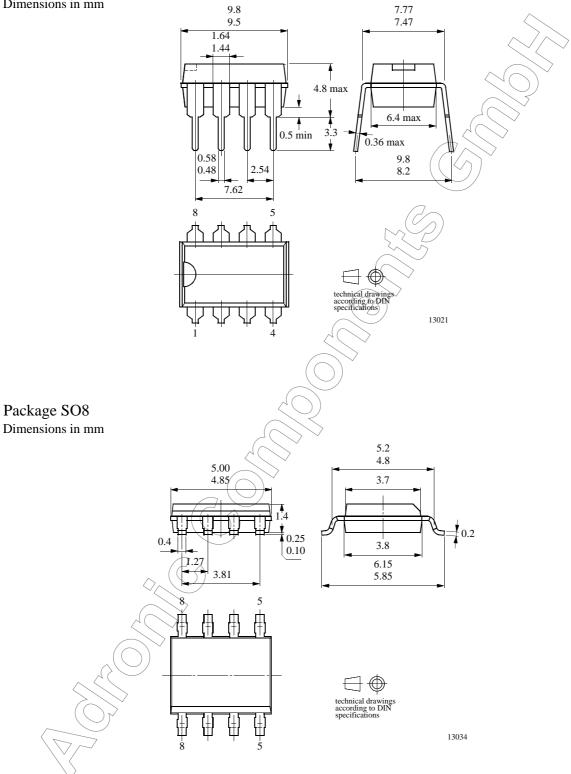
Figure 12. Application with limit switch



Package Information

Package DIP8

Dimensions in mm



Ozone Depleting Substances Policy Statement

It is the policy of **TEMIC TELEFUNKEN microelectronic GmbH** to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

TEMIC TELEFUNKEN microelectronic GmbH semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

TEMIC can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice. Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use TEMIC products for any unintended or unauthorized application, the buyer shall indemnify TEMIC against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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